

**Broadband solutions for** critical communication requirements

# **Eclipse MDI Detectors Terms and Definitions**





Not actual size

### **Biased Schottky**

- High Dynamic Range
- Flat Frequency Response
- Ultra Wideband
- Low Video Out Resistance

### Threshold

- 10dB Dynamic Range
- Min. Pulse Width: 50*n*S typ.
- TTL Logic Output
  - DC Power (No Load): <u>+</u>5VDC @ 100mA

**RoHS Compliant** 

### **Tunnel Diode**

- **No Bias Required**
- **Excellent Frequency Response**
- Wide Dynamic Range
  - Extremely Fast Pulse Response •

#### **High Reliability**

#### **Zero Bias**

- **Negative Polarity**
- **Extremely Fast Pulse Response**
- **No Bias Required**
- High Voltage Output Sensitivity



www.eclipseMDI.com

1.408.526.1100

EclipseMDI sales@eclipsemdi.com



#### Voltage Sensitivity: K-Factor

This parameter describes the slope of the transfer function of the diode or the ability to convert RF power into video voltage. This term is usually expressed with an open circuit video load. K-Factor is expressed in millivolts per milliwatts and is measured at -20 dBm, which is the top of the square law region. K values range from a few hundred to several thousand.

#### Video Resistance: Rv

Expressed in ohms, Rv is the real part of the output impedance measured while RF power and bias (if required) is applied. Therefore, this is the dynamic output resistance. Rv is calculated by simply measuring the open circuit K-Factor in the square law region. Then, a load resistor is applied and adjusted until the output K-Factor is reduced by half.

#### Figure of Merit: M

This parameter is a combination of both K-Factor and R<sub>v</sub>. M is a quality factor, which relates the detector's sensitivity to video load conditions.

#### $M = K/R_v$

Figure of Merit (M) is the denominator in the calculation of tangential sensitivity power input. A high figure of merit (M) value indicates a improved  $T_{ss}$  performance.



Square Law Equivalent Circuit





#### Tangential Sensitivity: T<sub>ss</sub>

Tangential sensitivity is the measurement of combined detector and video amplifier performance as a receiver. T<sub>ss</sub> is controlled by temperature, video amplifier noise figure and M of the detector.

T<sub>ss</sub> has been accepted in the industry as the signal power that creates an 8 dB signal-to-noise ratio at +27 °C.

 $PT_{ss} = ([3.22/\sqrt{BWxNF})]/M) \times 10^{-7}$ 

where,

PT<sub>ss</sub> = RF power at Tss in milliwatts BW = Video amp bandwidth in Hz NF = Video amp noise figure (Ratio)

 $T_{ss}$  is measured with a video amplifier having at least 50 dB gain, < 3 dB noise figure, and a 2 MHz video bandwidth. Measured  $T_{ss}$  is degraded using a video amplifier having greater than 2 MHz video bandwidth as:

 $-dB = 10 \log_{10} \sqrt{(Higher BW MHz/2MHz)}$ 

Measured  $T_{ss}$  is improved using a video amplifier having less than 2 MHz video bandwidth as follows:

 $+dB = 10 \log_{10} \sqrt{2 MHz/Lower BW MHz}$ 

#### Video Capacitance: C<sub>v</sub>

 $C_v$  is the imaginary part of the detector output impedance. It is mainly comprised of the RF bypass capacitance. The diode chip capacitance is typically < 1/200 of the bypass capacitor and is negligible. This capacitance may be selected at time of manufacture to optimize rise time and bandwidth while slightly degrading video isolation.

#### Flatness

Flatness is the variation of input RF power required across the frequency range to produce a constant output voltage at the video load. This is typically measured at -20 dBm while measuring the K-Factor.



www.eclipseMDI.com 1.408.526.1100



#### Rise Time (t,) - Video Bandwidth

Pulse response systems, consideration to video pulse quality, are of extreme importance and frequently have a trade off between Tss and video bandwidths. Detector video bandwidth depends on video impedance and varies considerably with the diode type and RF bypass capacitor used.

The 10% to 90% rise tme for the output voltage across the video load R due to a step RF power rise at the detector input is:

 $t_r(10-90\%) = 2.2 \times [R_v R_L /(R_v + R_L)] \times (C_v + C_L) = 0.35/BW$ 

where,

 $R_v$  = Detector video resistance

- $C_v$  = Detector circuit bypass capacitor
- $C_{L}$  = Load capacitor
- R = Load resistance
- BW = Circuit 3 dB video bandwidth (Hz)

Typical values of  $R_v$  range from 75 to 125  $\Omega$  for tunnel diode detectors and 200 to 800  $\Omega$  for Schottky barrier diode detectors.



- www.eclipseMDI.com
- **1.408.526.1100**



#### **Dynamic Range**

The dynamic range is defined differently depending on application. Expressed in dB, dynamic range extends from  $T_{ss}$  at the low end to either the top of the square law region or the maximum safe power handling of +20 dBm (+17 dBm for tunnel diodes). The safe power handling range may be increased by the use of a limiter. This usually causes saturation of output at less than +20 dBm while allowing safe inputs of over a watt of C/W power. Detector input padding will increase the upper power end of the dynamic range by the value in dB of the padding. This will shift up the lower end of the dynamic range.

#### **Transfer Function Curve**

This is the plot of a detector output voltage versus RF input power. A single graph usually includes transfer function curves of detector with several different load values. Typical transfer function plots are shown in the appropriate section for each detector diode type.

#### **Temperature Stability**

Signal power variation is required to maintain a constant output video voltage as temperature is varied. The delta is expressed in ±dB. This parameter is usually measured and specified at the top of the square law region. Video amplifier designers have designed compensation circuits that greatly reduce the effects of detector output variations over the required operating temperature. Variations with temperature are not constant above the square law region. Example: A zero-bias Schottky detector varies ±1.5 dB to ±2 dB from -55 °C to +125 °C in the square law region. However, at +10 dBm, the same detector would vary only ±0.35 dB over the same temperature range.

#### Video Isolation

Input RF power is bypassed to ground on the video side of the detector chip. The RF bypass capacitance is selected to provide 20 to 30 dB of isolation at the video port for the RF input power. When increased rise time and improved video bandwidth is desired, a lower RF bypass capacitance is required. This degrades the isolation, which can cause increased fine grain frequency response, and in some cases reduced sensitivity.

#### Voltage Standing Wave Ratio: VSWR

The Voltage Standing Wave Ratio at the detector's RF input port varies considerably with diode type, incident input power level and video load. Typically, the VSWR is enhanced with the application of bias, but degrades quickly above the square law region. Padding and video load selections enhance VSWR when used for instrumentation.

#### Tracking

Detector may be supplied as pairs, triples, quads, or many units per set. Tracking is the maximum variation from unit to unit in a set. This K-Factor between the units in any set is usually tested over the specified frequency range at -20 dBm input power. Octave band or waveguide bandwidths may be matched to ±0.3 dB at lower frequencies and ±0.5 dB at frequencies up to 18 GHz. Broadband and higher frequencies 40 GHz units may be matched to ±0.7 dB to ±1.0 dB. Detector match sets are more common with Tunnel Diode Detectors. A key electrical parameter in supplying match Tunnel Detectors is to match the I/V curve of the diode. The I/V curve of a Tunnel Diode is show below.



Typical Tunnel Diode Curve

💮 www.eclipseMDI.com

■ 1.408.526.1100



#### How to specify and select detectors

This section includes a selection table as a guide to the performance parameters of the different types of detectors designed and manufactured at Eclipse MDI. This includes the four-diode types we use.

Table 1 shown below presents a comparison of performance for the different diode types.

TABLE 1: PERFOMANCE COMPARISON OF MICROWAVE DETECTORS												
Detector Type Performance	6 dB Padded Biased Schottky	6 dB Padded Zero-Bias Schottky	3 dB Padded Tunnel Diode	Biased Schottky	Zero-Bias Schottky	Germanium Tunnel	GaAs Tunnel					
Circuit Diagrar	: R n Ing	Fout 500		Video Output	RF Input	dc Return	OUT Video - Output					
Bias (µA)	50	0	0	100 to 300	0	0	0					
Tangential Sensitivity Tss (dBm)(2MHz VBW, NF = 3dB)	-45	-45	-47	-52 to -50	-52 to -50	-51 to -49	-49					
Voltage Sensitivity K (mV/mW)	500 to 1000	500 to 1000	300 to 600	1000 to 3000	1500 to 3000	400 to1200	250 to 800					
Video Resistance Rv Ω (Square Law Region)	800 to 1200	400 to 2000	100 to 200	150 to 500	400 to 2000	400 to 1200	100 to 150					
Input VSWR(Square Law Region)	1.5:1	1.5:1	2:1	2:1 to 6:1	2:1 to 6:1	1.5:1 to 3:1	3:1					
Frequency Response (Square Law Region) Octaves (dB)	±0.3 to ±0.6	±0.3 to ±0.6	±0.3 to ±0.5	±0.4 to ±0.7	±0.4 to ±0.7	±0.3 to ±0.7	±0.3 to ±0.5					
Frequency Response (Square Law Region) Decades (dB)	±1.0	±0.75	±0.7	±1.5	±1.5	±1.5	±1.5					
Temperature Stability(dB) (-55°℃ to 85 C)	±1.0	±2.0	±0.5	±1.0	±2.0	±0.5	±0.5					
Power Rating Maximum Safe (dBm)	+26	+26	+21	+20	+20	+17	+17					
Rise and Fall Time	Medium	Medium Slow	Fast	Medium	Medium Slow	Fast	Fast					

www.eclipseMDI.com

1.408.526.1100



#### About Eclipse MDI

ECLIPSE Microdevices is located in San Jose, California. ECLIPSE has been developing high performance analog semiconductors for use in wireless radio frequency (RF), microwave, and millimeter wave for commercial and industrial applications. ECLIPSE has formed a strategic alliances - with foundries that features leading state-of-the-art process technologies and with manufacturing facilities for high-volume production of innovative RFIC's.

#### How to specify and select detectors

This section includes a selection table as a guide to the performance parameters of the different types of detectors designed and manufactured at Eclipse MDI. This includes the four-diode types we use.

Table 2 shown below provides a qualitative selection guide of detectors for use in low-level detection such as crystal video applications.

TABLE 2: LOW LEVEL MICROWAVE SIGNAL DETECTOR SELECTION CHART												
Most Critical Parameter	Best Selection Detector Diode Type	Tss	VSWR (Square law Region)	Power Rating	Temperature Range	Temperature Stability	Video Resistance (Square Law)	Bias Requires				
Tss (Tangential Sensitivity)	Regular Schottky	Excellent	Fair	Fair	Excellent(+125°C)	Fair	Medium (Good)	Yes				
	Zero-Bias Schottky	Excellent	Poor	Poor	Excellent(+125 <sup>0</sup> C)	Poor	High (Poor)	No				
	Limiter-Schottky	Excellent to Good	Fair	Excellent	Excellent(+125 <sup>0</sup> C)	Fair	Medium (Good)	Yes				
	Limiter Zero Bias Schottky	Excellentto Good	Fair	Excellent	Excellent(+125 <sup>0</sup> C)	Poor	High (Poor)	No				
	Multi-Diode Schottky	Excellent	Very Good	Very Good	Good(+100 <sup>0</sup> C)	Fair	Medium High (Fair)	Yes				
VSWR(Square Law Region)	Padded-Schottky	Poor	Excellent	Very Good	Excellent(+125 <sup>0</sup> C)	Fair	Medium (Good)	Yes				
	Padded Zero-bias Schottky	Poor	Excellent	Very Good	Excellent(+125 <sup>0</sup> C)	Poor	High (Poor)	No				
	Multi-Diode Schottky	Excellent	Very Good	Very Good	Good(+100 <sup>0</sup> C)	Fair	Medium (Fair)	Yes				
Power Handling	Padded-Schottky	Poor	Excellent	Very Good	Excellent(+125 <sup>0</sup> C)	Fair	Medium (Good)	Yes				
	Padded Zero-Bias Schottky	Poor	Excellent	Very Good	Excellent(+125 <sup>0</sup> C)	Poor	High (Poor)	No				
	Multi-Diode Schottky	Excellent	Very Good	Very Good	Good(+100 <sup>0</sup> C)	Fair	Medium (Fair)	Yes				
	Limiter-Schottky	Excellent to Good	Fair	Excellent	Excellent(+125 <sup>0</sup> C)	Fair	Medium (Good)	Yes				
Temperature Rating	Regular Schottky	Excellent	Fair	Fair	Excellent(+125 <sup>0</sup> C)	Fair	Medium (Good)	Yes				
	Zero-Bias Schottky	Excellent	Poor	Poor(+125 <sup>0</sup> C)	Excellent	Poor (Poor)	High	No				
	Limiter-Schottky	Excellent to Good	Fair	Excellent	Excellent(+125 <sup>0</sup> C)	Fair	Medium (Good)	Yes				
Temperature Stability	Tunnel	Good	Good	Poor	Fair(+85 - +100 <sup>0</sup> C)	Excellent	Low (Excellent)	No				
Low Video Resistance No BiasingRequired	Tunnel	Good	Good	Poor	Fair(+85 - +100 <sup>0</sup> C)	Excellent	Low (Excellent)	No				
	Tunnel	Good	Good	Poor	Fair(+85 - +100 <sup>0</sup> C)	Excellent	Low (Excellent)	No				
	Zero-Bias Schottky	Excellent	Poor	Poor	Excellent(+125 <sup>0</sup> C)	Poor	High (Poor)	No				

www.eclipseMDI.com

1.408.526.1100



### Zero Bias Shottky Detector - Input Matched - EZM Series



Zero Bias Shottky Detector - High Sensitivity - EZR Series



Quality products that serve the industry. Today and tomorrow. Copyright EclipseMDI www.eclipseMDI.com



#### **Tunnel Diode Detector - Planar - DT Series**







### Threshold Detector - Regulated Vset Curve - TH Series





www.eclipseMDI.com 1.408.526.1100



#### **Detector Outline Drawings**





T1 Ø0.012 [Ø0.30] GOLD PLATED PIN 0.090 [2.29] \_ TYP. NOTES, UNLESS OTHERWISE SPECIFIED. 1. DIMENSIONS ARE IN INCHES[MM] 2. MATERIAL: STAINLESS STEEL 0.190 [4.83] 3. FINISH: BODY & PIN: GOLD CONNECTOR & BODY: PASSIVATED 0.095 [2.41] 0.125 [3.17] 3X, EQUALLY SPACED 4 4 Ø0.313 [Ø7.95] TYP. 0.375 [9.53] Ø0.025 [Ø0.64] GOLD PLATED BIAS PIN GROUND 0.495 [12.56] TYP 0.203 [5.16] TYP 50 [1.52] - 0.155 [3.94] Vset +51 -51 Đ 0.220 [5.59] 0.550 [13.97] Z Ë 4 0.375 [9.53] 2X, SMA(F) CONN 0.630 [16.00] Ø0.079 [Ø2.01] 4X, THRU HOLE 0.750 [19.05]

1.408.526.1100